

FIG. 1

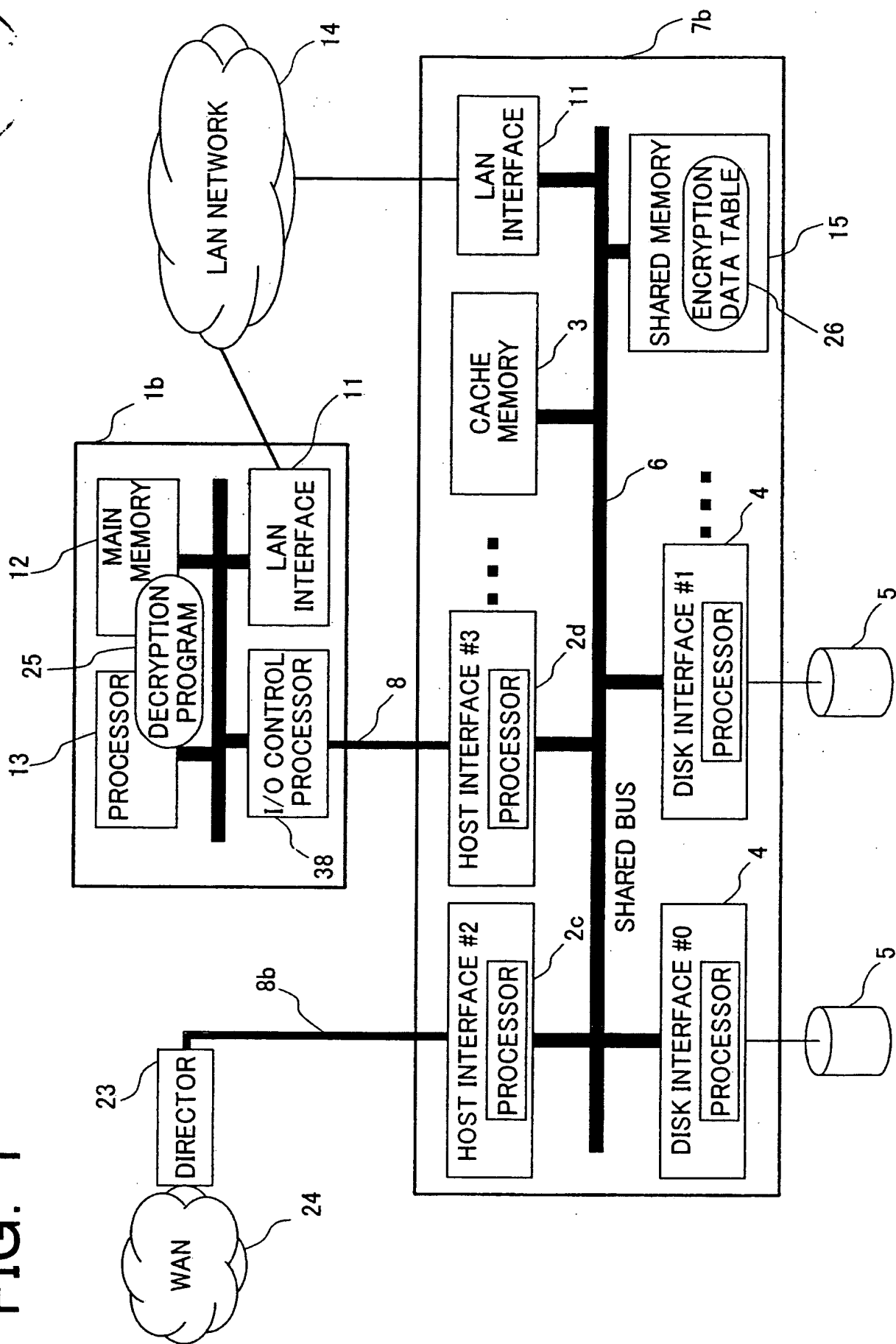


FIG. 2

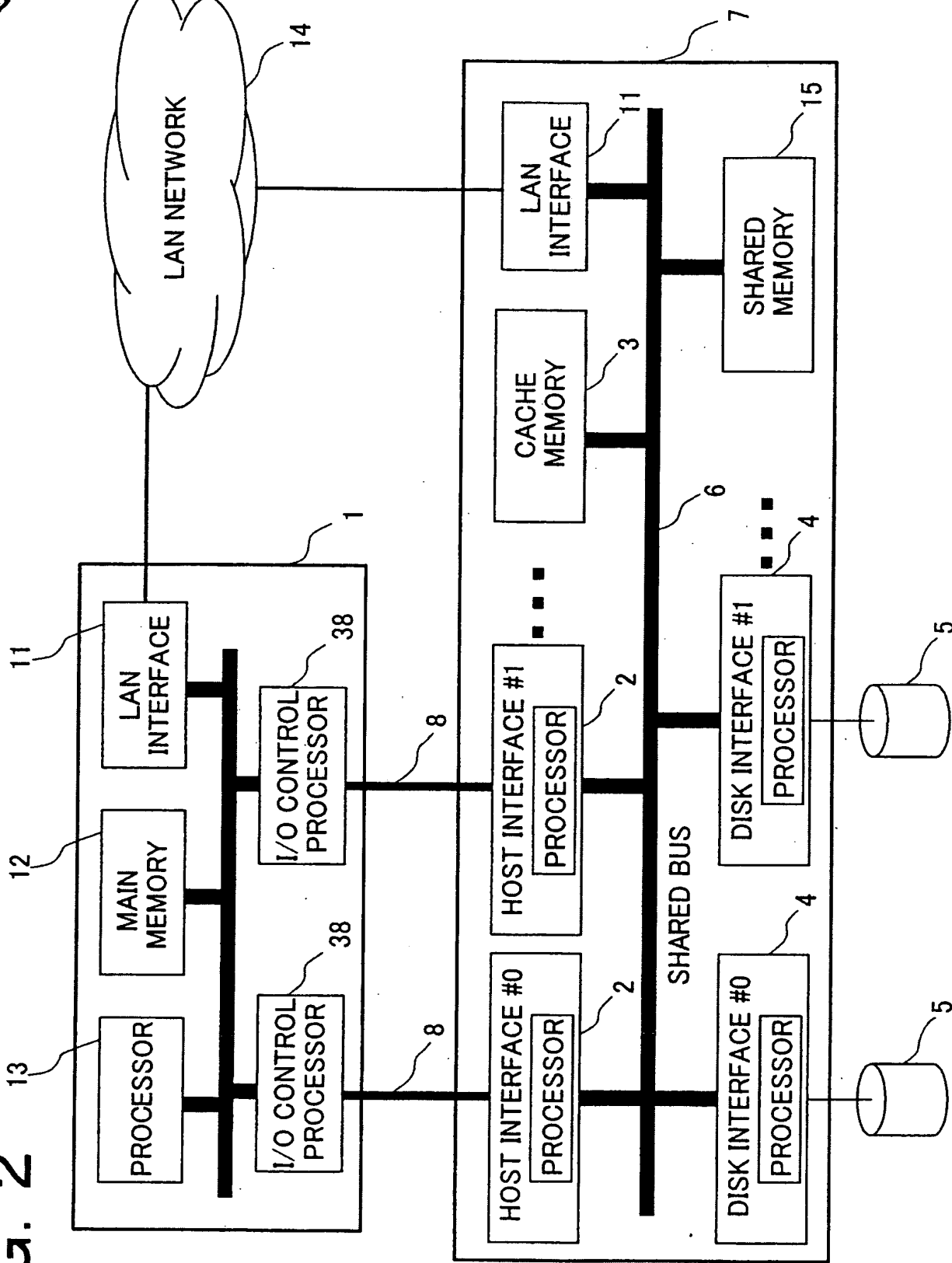


FIG. 3

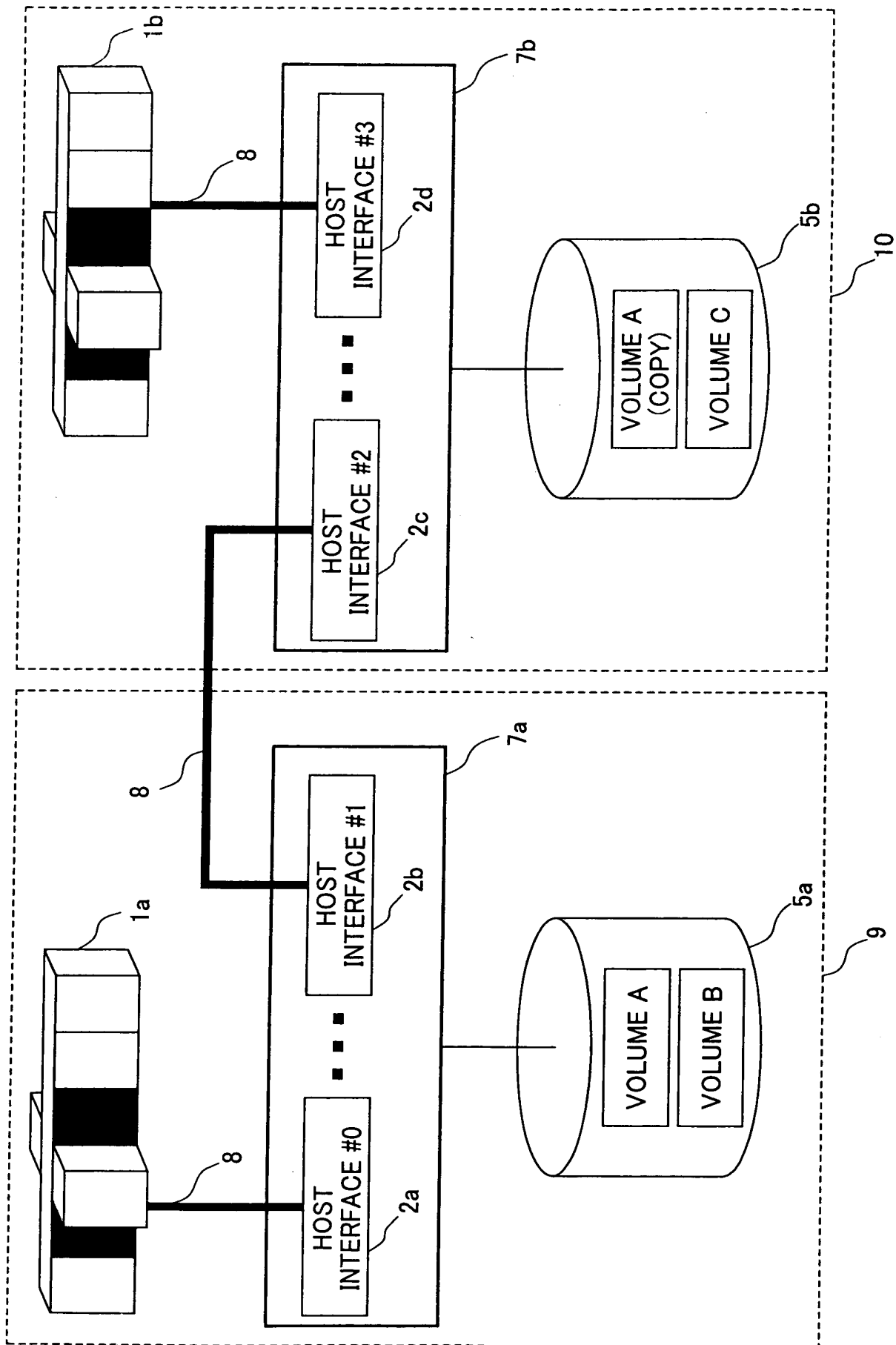


FIG. 4

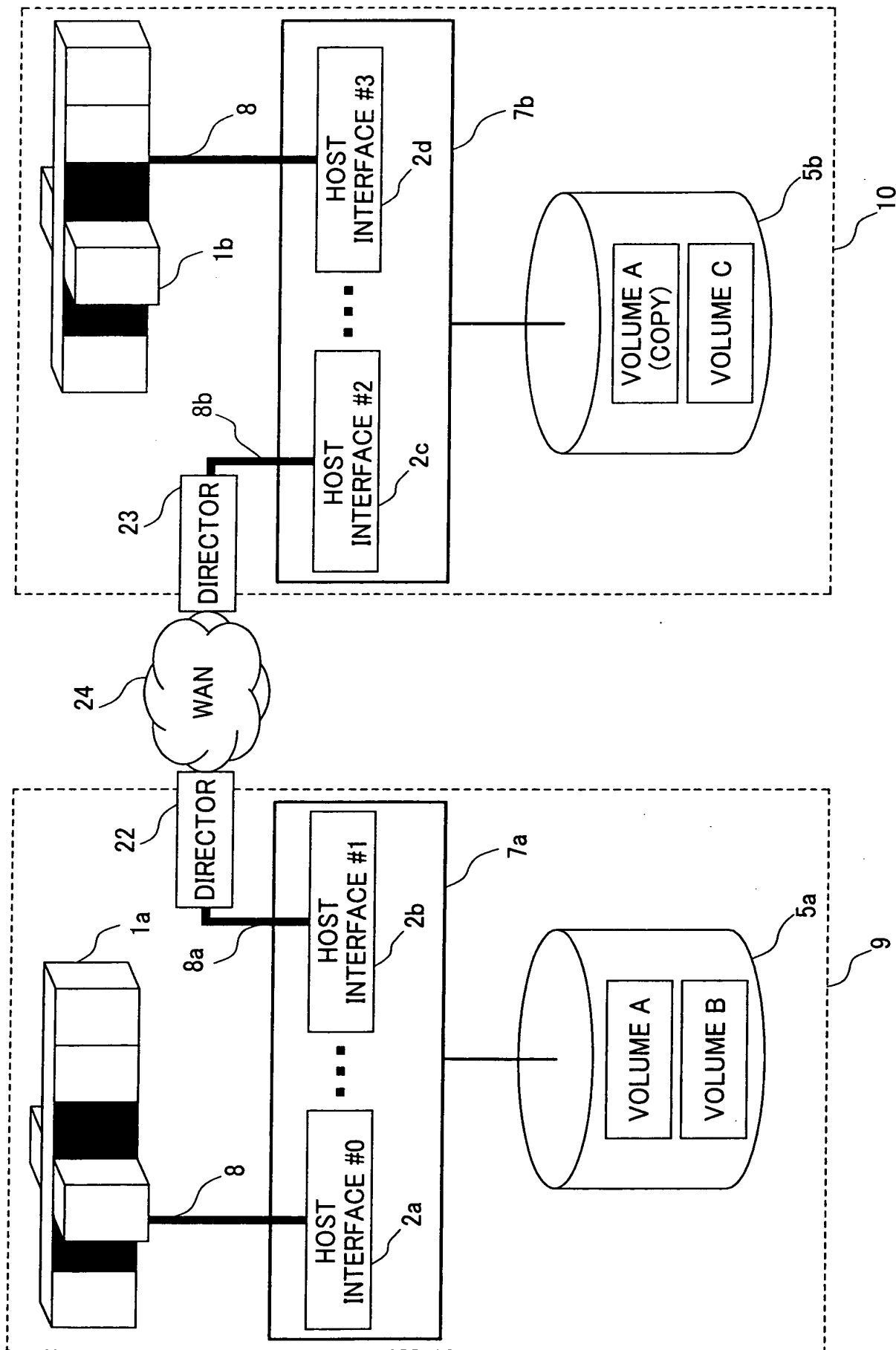




FIG. 5

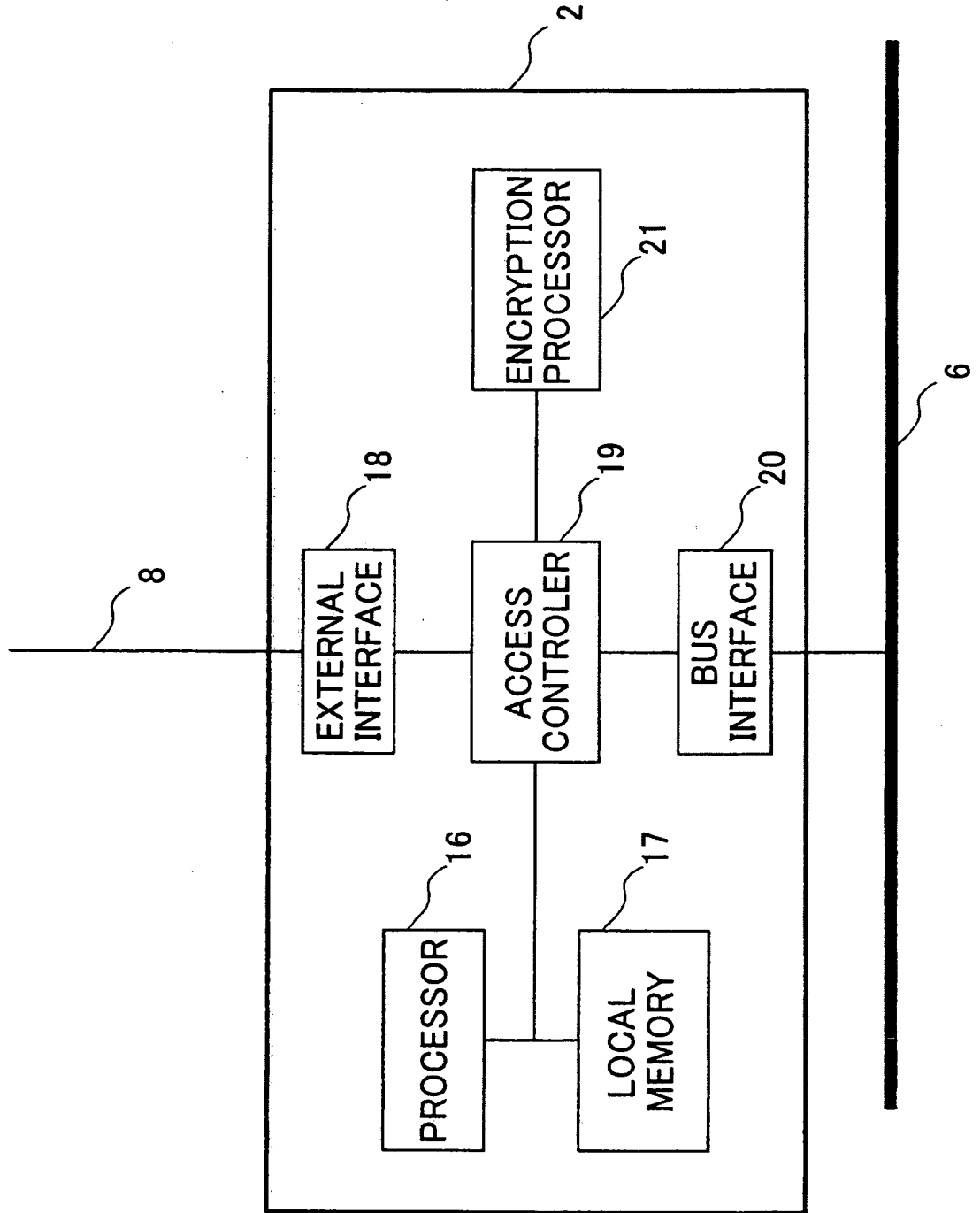




FIG. 6

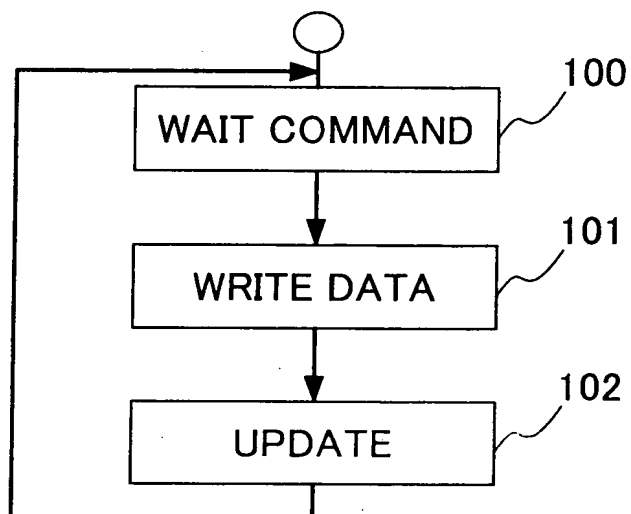


FIG. 7

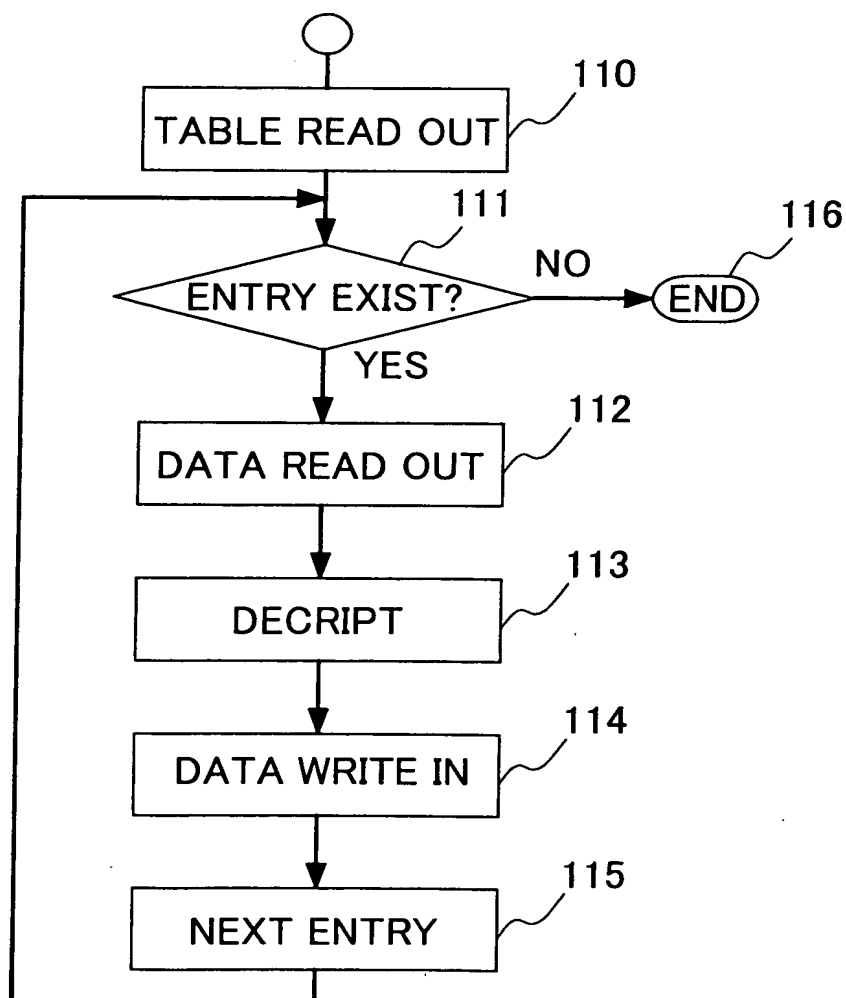




FIG. 8

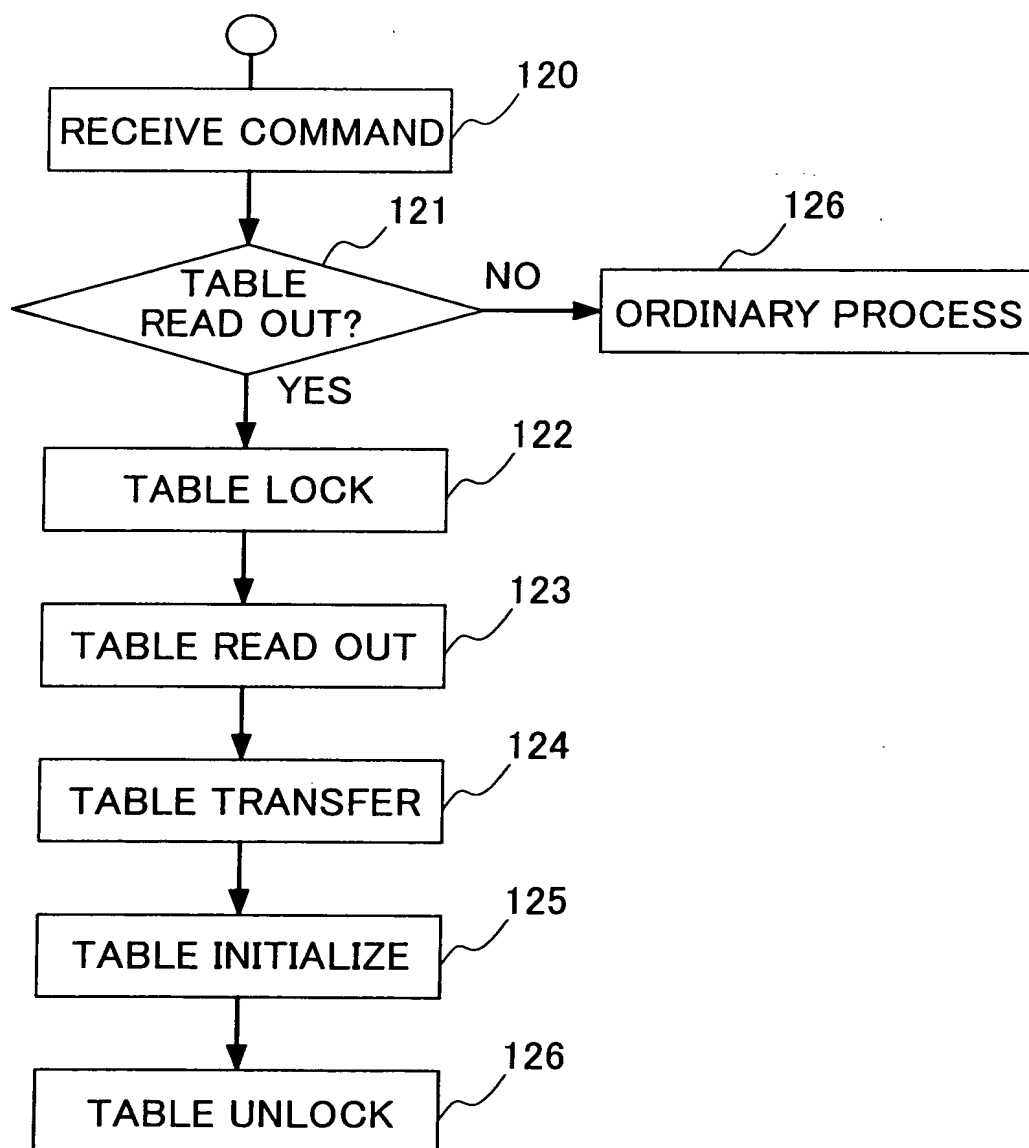




FIG. 9

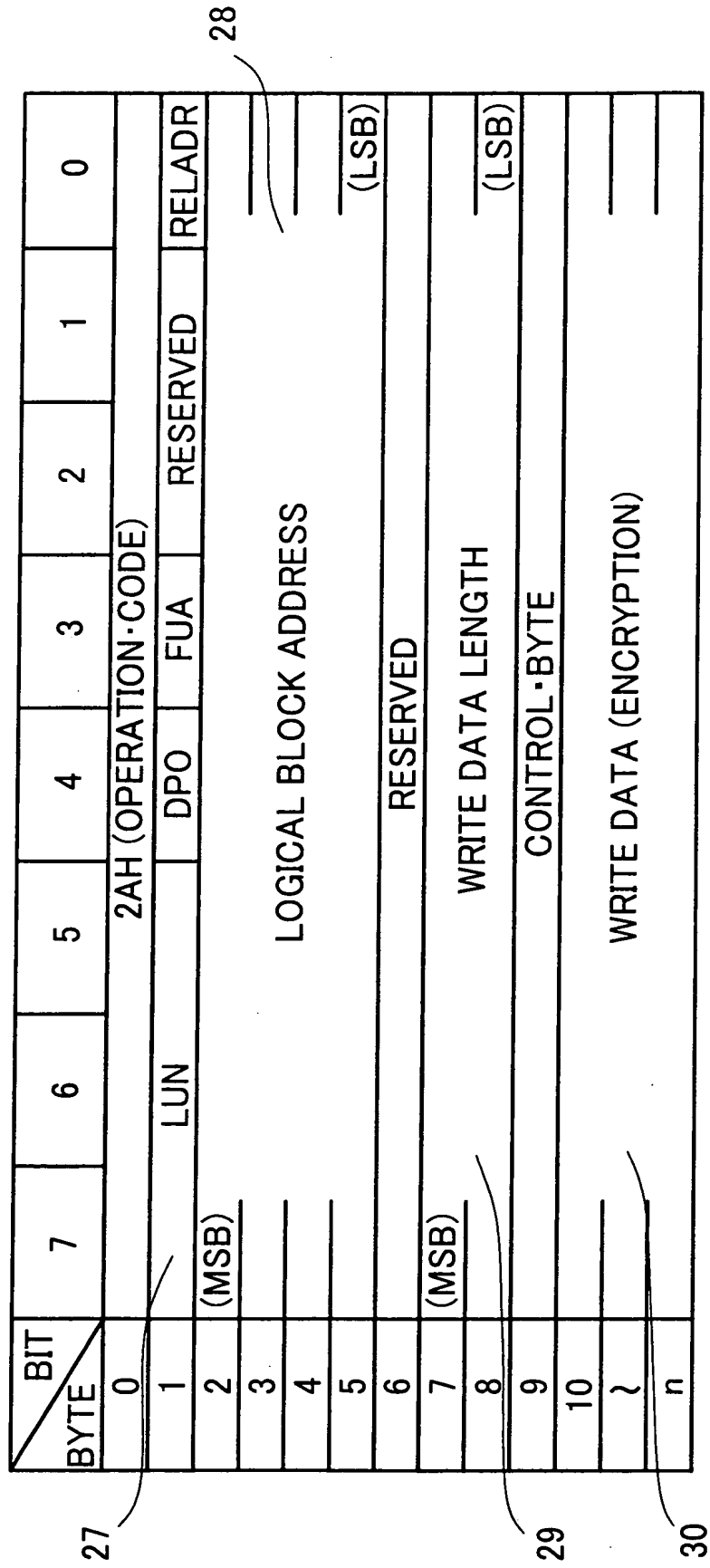






FIG. 10

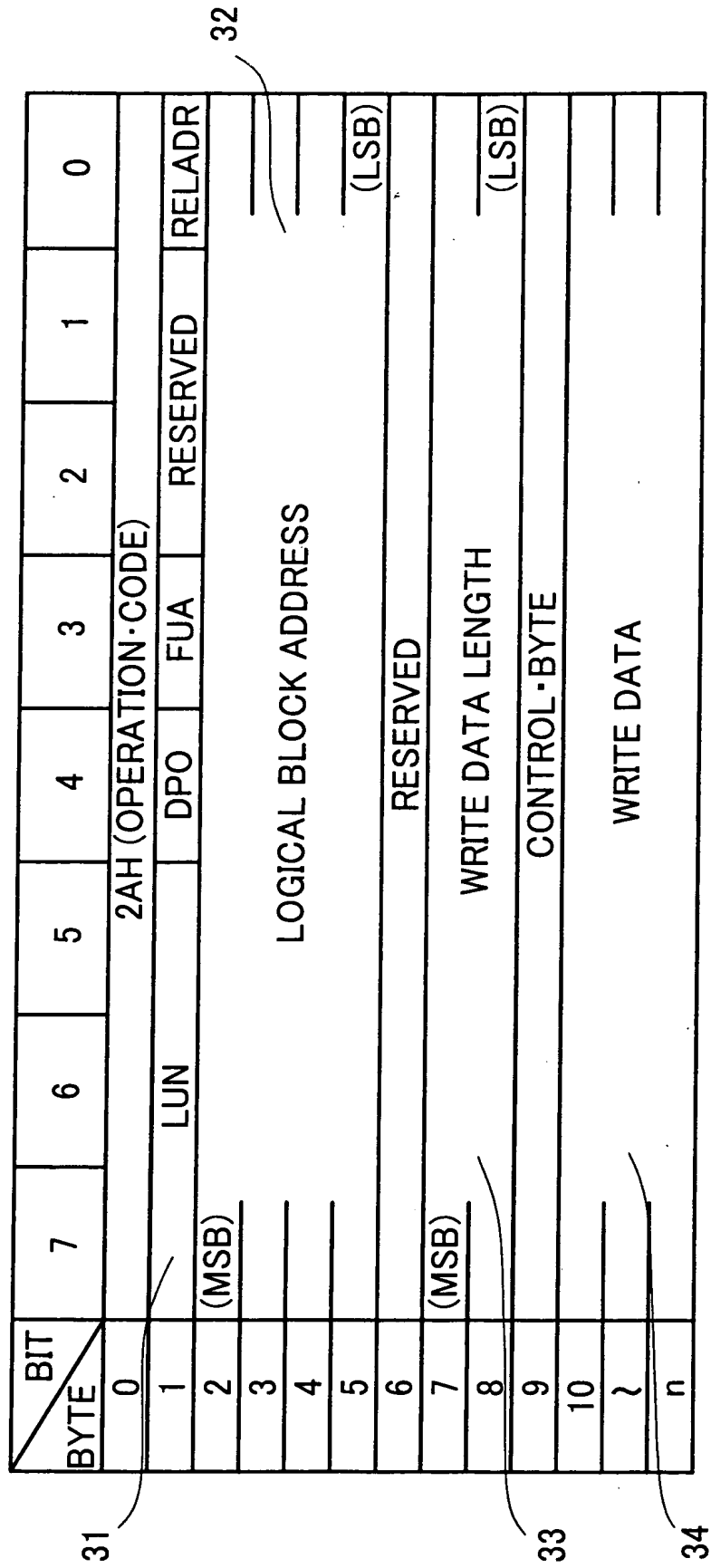




FIG. 11

35		36		37
LUN NUMBER	LOGICAL BLOCK ADDRESS	WRITE DATA LENGTH		
0	10	100		
5	123	789		
	{			
1	94	20		
-1				

FIG. 12

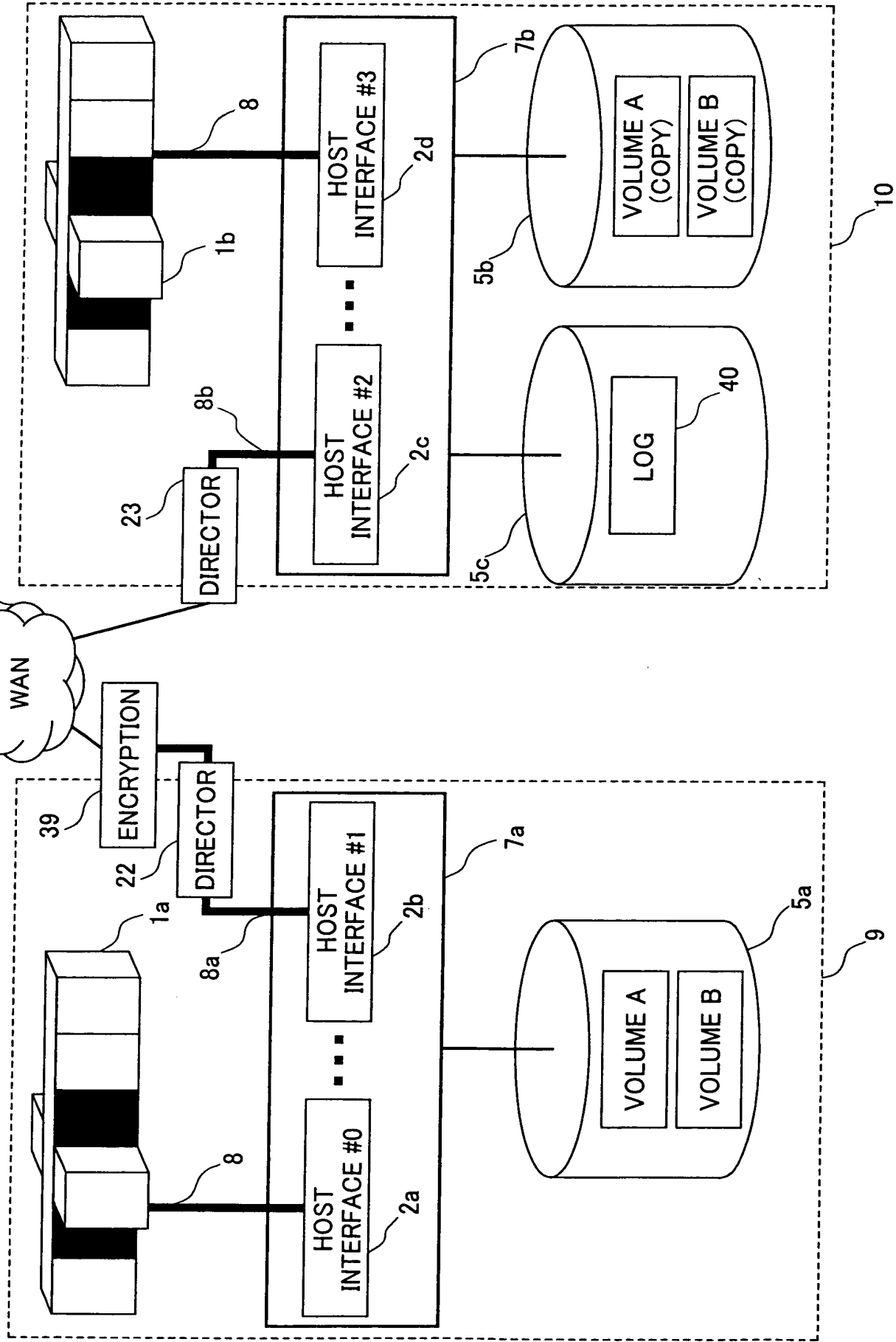




FIG. 13

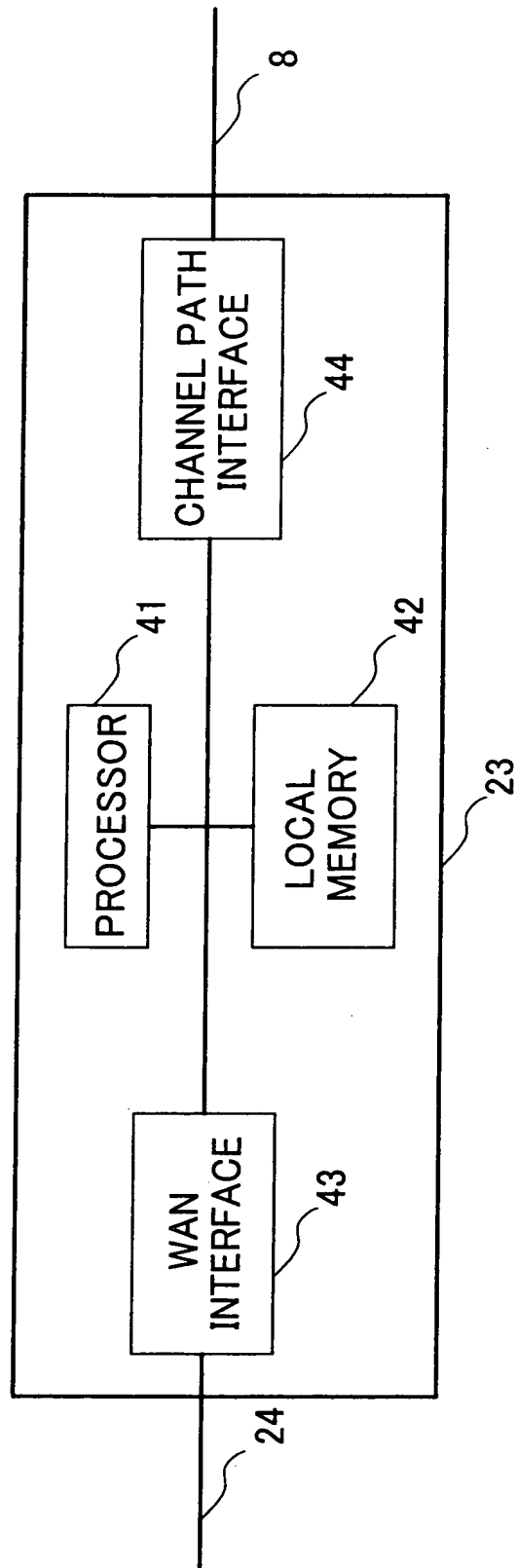


FIG. 14

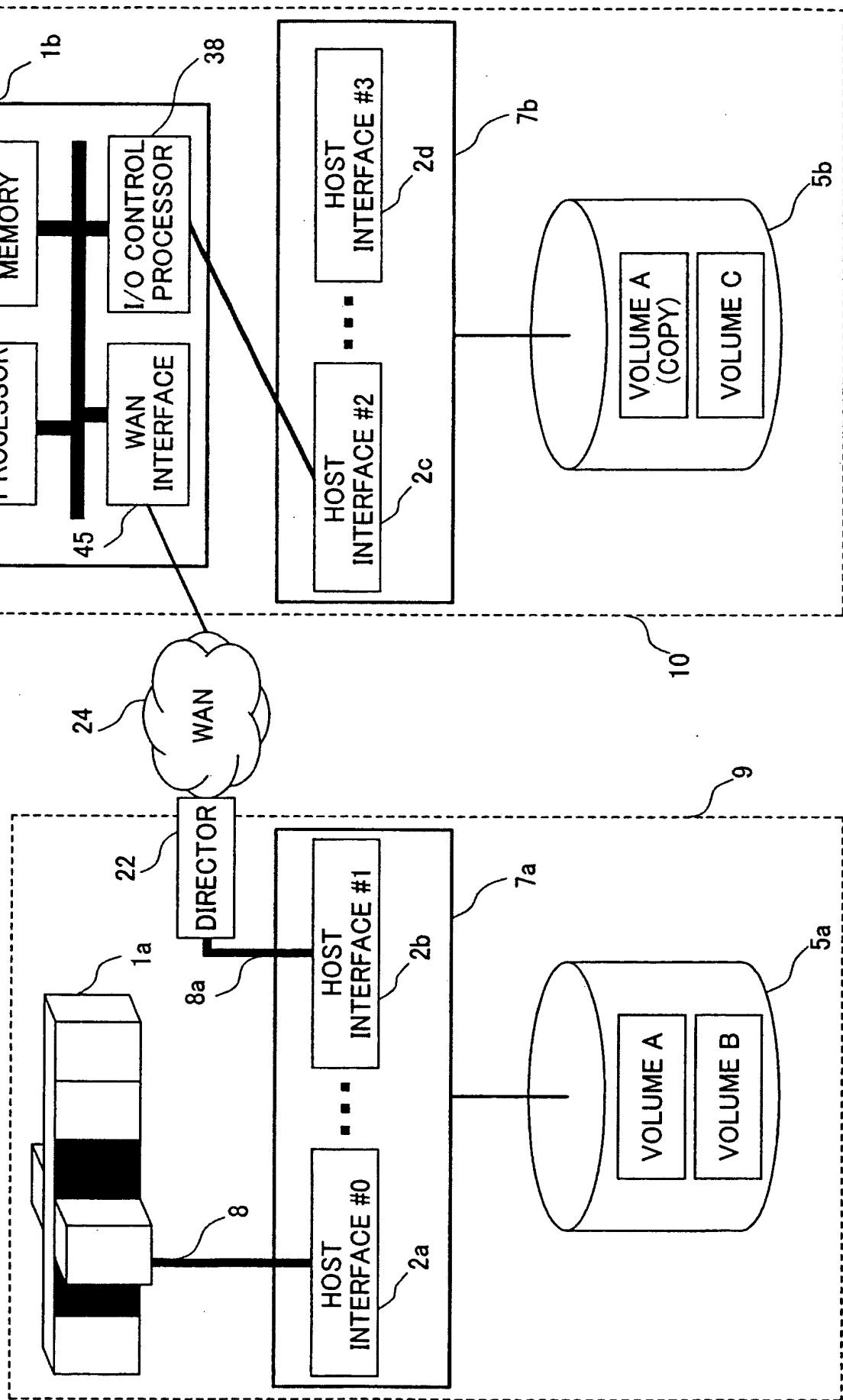




FIG. 15



FIG. 16

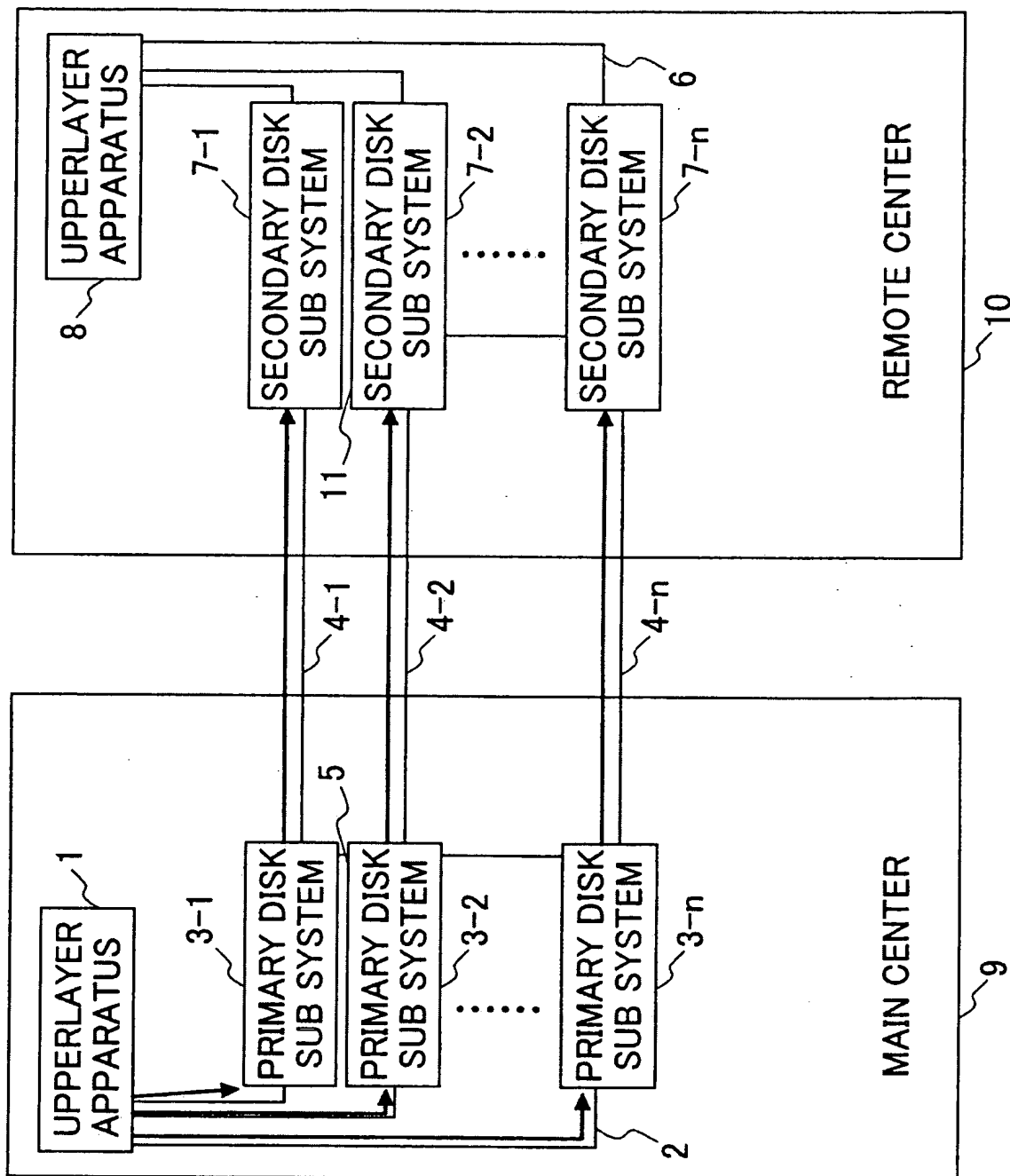




FIG. 17

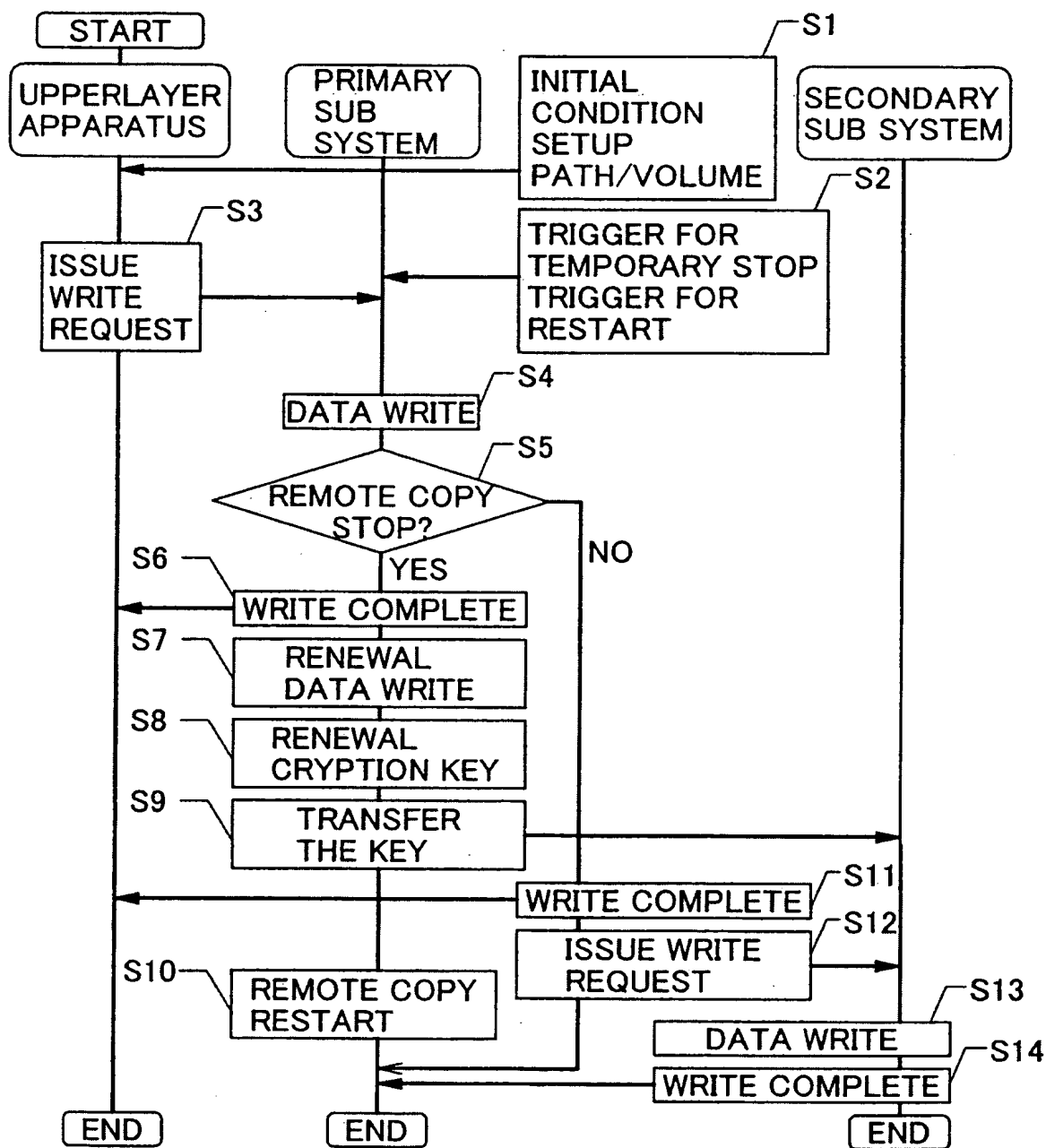
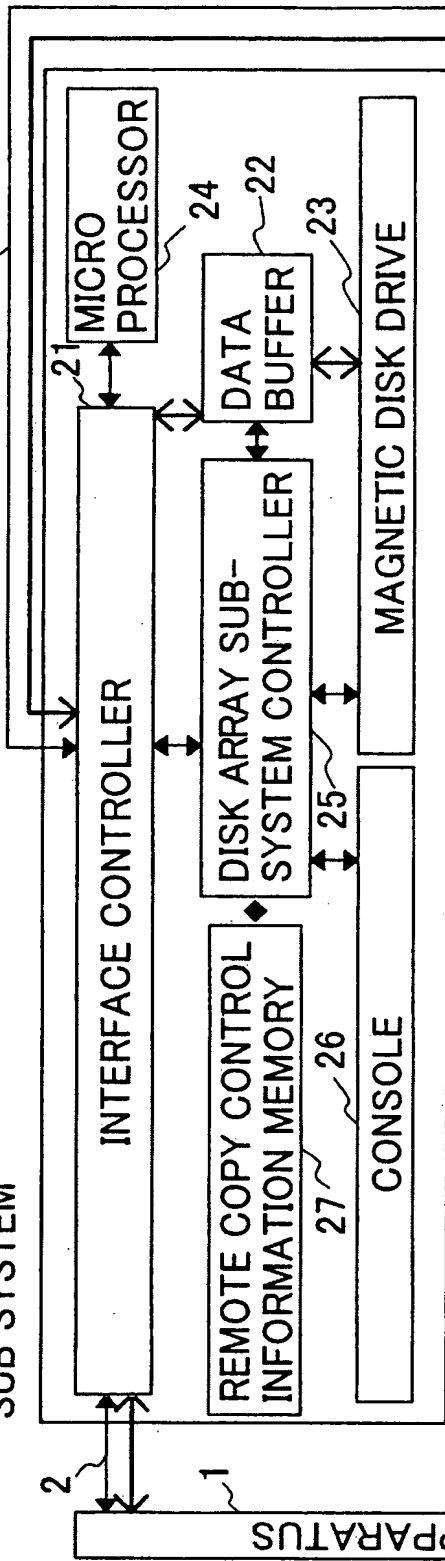


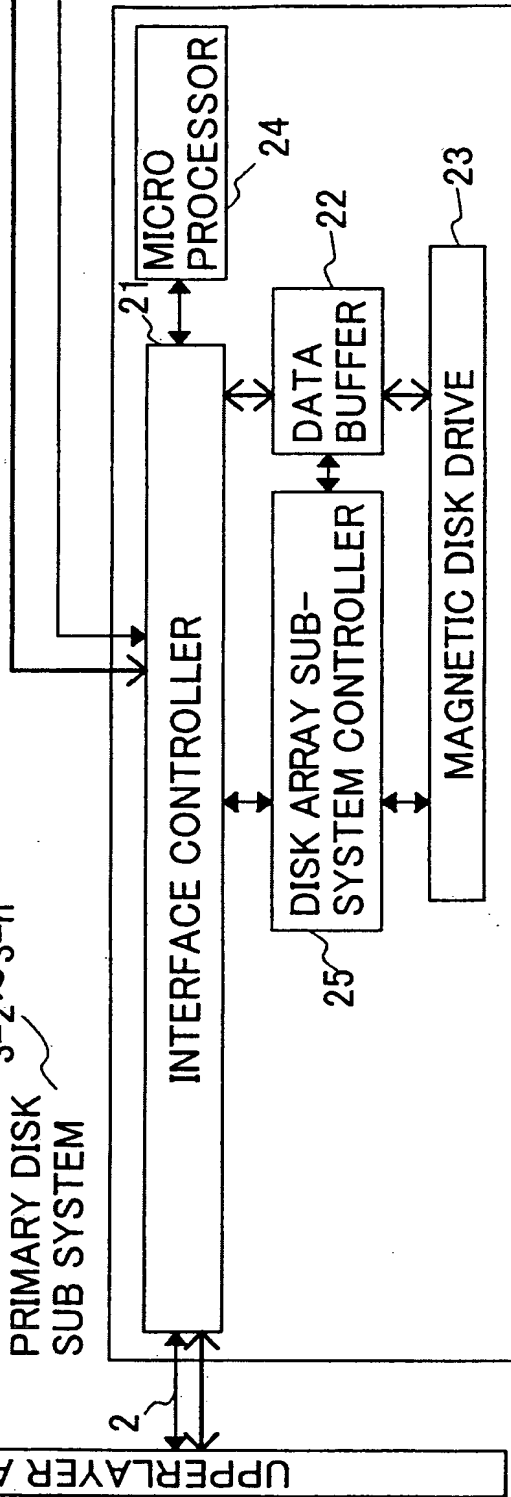


FIG. 18

PRIMARY DISK SUB SYSTEM 3-1



PRIMARY DISK SUB SYSTEM 3-2~3-n



↔ CONTROL DATA FLOW  
 ↔ DATA FLOW





FIG. 19

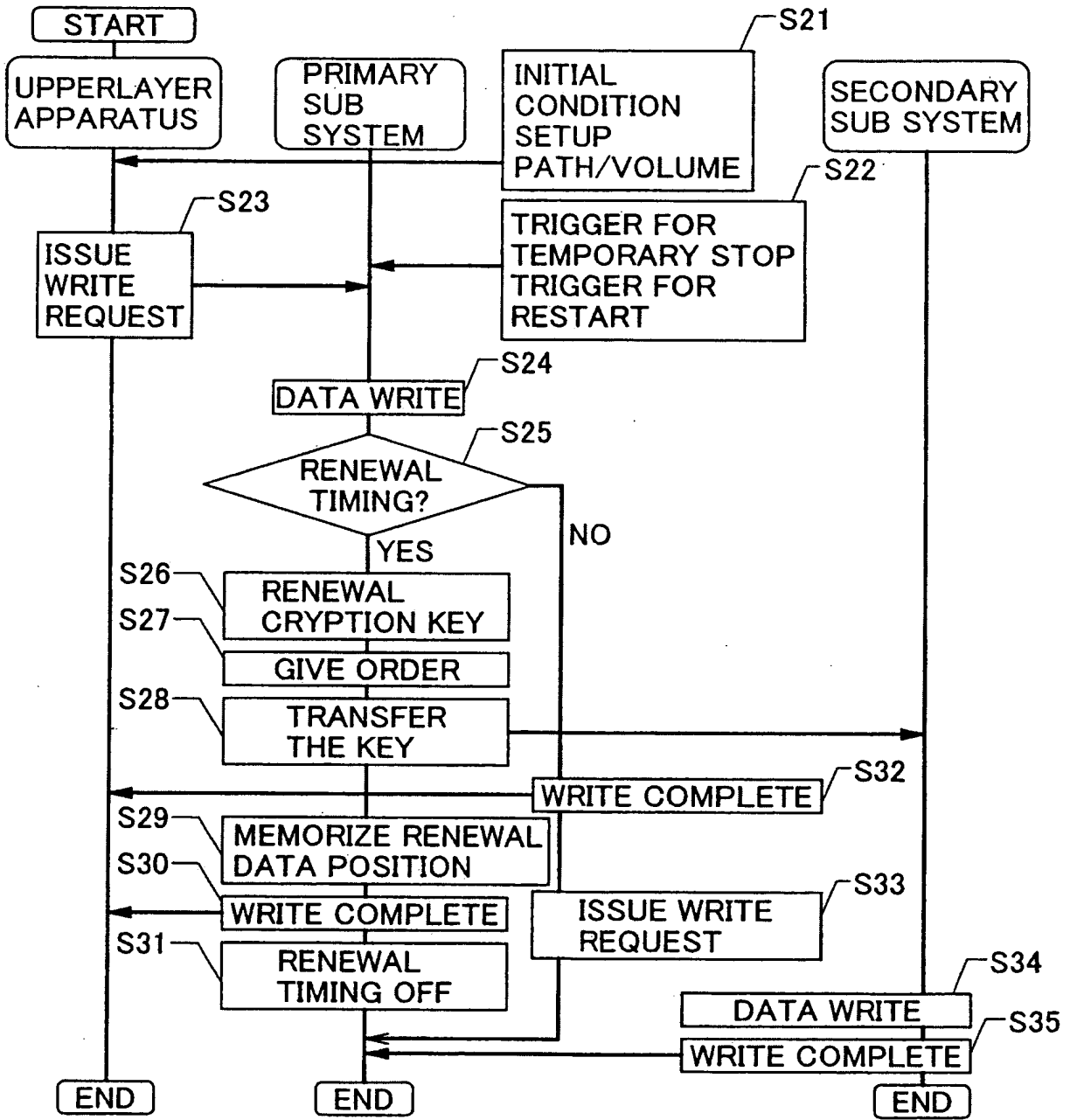




FIG. 20

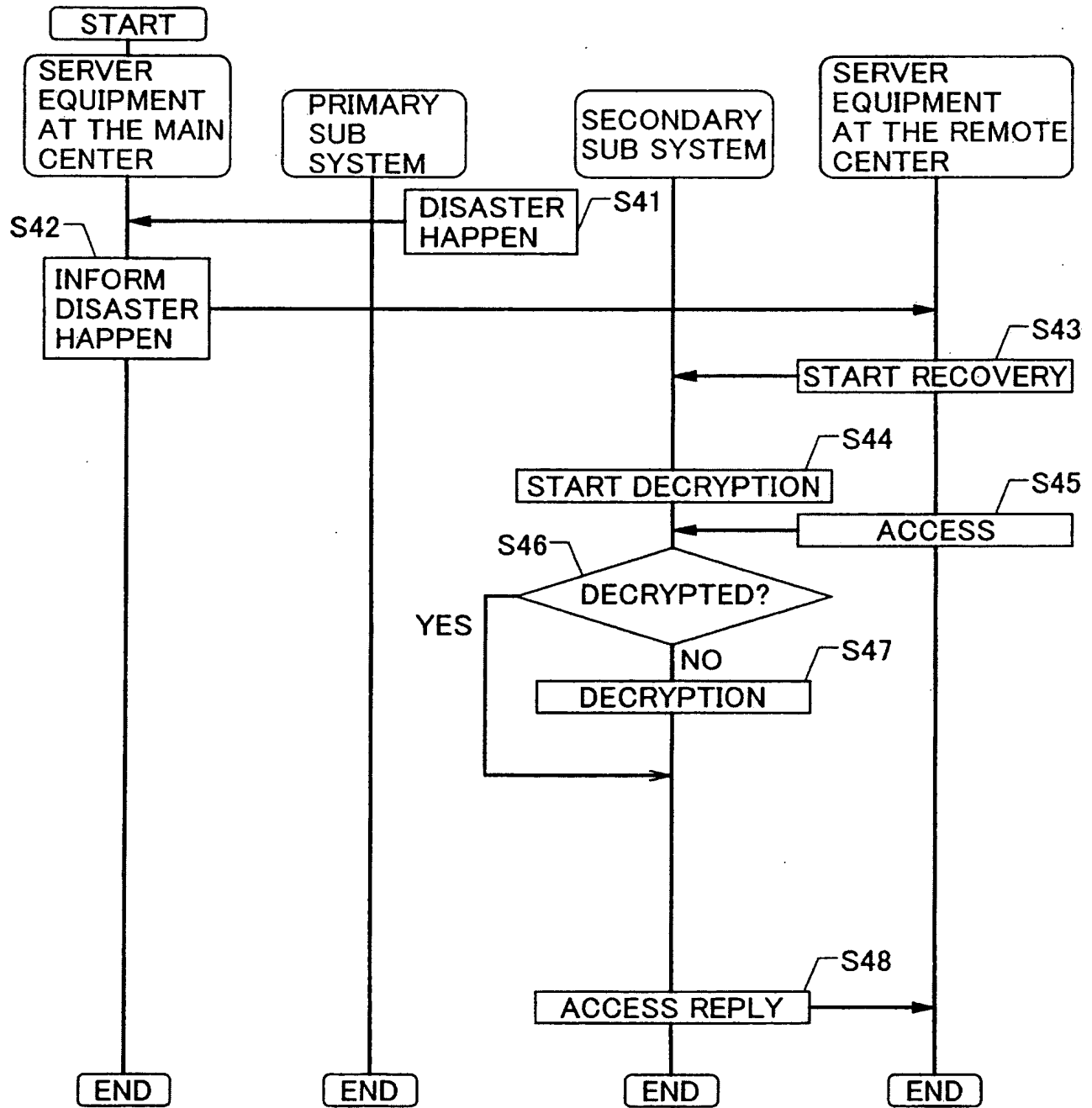




FIG. 21

